

IN THE CLAIMS

Claim 1 (currently amended): A driver circuit, comprising:
a differential first pair of transistors to input a differential signal;
a voltage drive stage comprising a second pair of transistors;
a first pair of nodes coupled to the first pair of transistors and to the second pair of transistors;
a second pair of nodes coupled to the second pair of transistors; and
a current switch coupled to the second pair of nodes to steer current based the polarity of said first pair of input nodes.

Claim 2 (original): The driver circuit of claim 1 further comprising a first pair of resistors coupled to the first pair of nodes.

Claim 3 (original): The driver circuit of claim 2 further comprising a voltage regulator coupled to the first pair of resistors.

Claim 4 (original): The driver circuit of claim 1 further comprising a load resistor coupled to the second pair of nodes.

Claim 5 (original): The driver circuit of claim 1 further comprising a second pair of resistors coupled to the second pair of nodes.

Claim 6 (original): The driver circuit of claim 5 further comprising a pair of output nodes coupled to the second pair of resistors.

Claim 7 (original): The driver circuit of claim 5 further comprising an output voltage coupled to the second pair of resistors.

Claim 8 (original): The driver circuit of claim 1 further comprising three serially coupled transistor and resistor pairs, wherein the first serially coupled

transistor is coupled to an input node, wherein the second serially coupled transistor is coupled to the differential first pair of transistors, and wherein the third serially coupled transistor comprises a portion of the dynamic current switch.

Claim 9 (original): The driver circuit of claim 8, wherein the first transistor is coupled to the second transistor.

Claim 10 (original): The driver circuit of claim 8, wherein the second transistor is coupled to the third transistor.

Claim 11 (previously presented): The driver circuit of claim 8, further comprising a ground, wherein the three resistors of the serially coupled transistor and resistor pairs are coupled to the ground.

Claim 12 (previously presented): The driver circuit of claim 8, further comprising an additional transistor coupled to the input node.

Claim 13 (original): The driver circuit of claim 1, wherein a first one of the first pair of nodes follows a first one of the second pair of nodes.

Claim 14 (original): The driver circuit of claim 1, wherein a second one of the first pair of nodes follows a second one of the second pair of nodes.

Claim 15 (original): The driver circuit of claim 1, further comprising a pair of input nodes coupled to the differential first pair of transistors and to the dynamic current switch.

Claims 16-26 (cancelled)